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APPLICANTS Akio Nishida, Tachikawa, JAPAN; Hiraku Chakihara, Akishima, JAPAN; Koichi Toba, Tachikawa, JAPAN;				
** CONTINUING DATA ***** <i>None</i>				
** FOREIGN APPLICATIONS ***** JAPAN 2003-044820 02/21/2003 <i>NEW</i>				
IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 05/12/2004				
Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 (a-d) conditions <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after met Verified and <i>Mark</i> Acknowledged Examiner's Signature Initials		STATE OR COUNTRY JAPAN	SHEETS DRAWING 87	TOTAL CLAIMS 12
				INDEPENDENT CLAIMS 4
ADDRESS 020457				
TITLE Semiconductor integrated circuit device and a method of manufacturing the same				
FILING FEE RECEIVED 1736	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:		<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit	